

# **METHOD FOR CONTROLLING OPTICAL POWER LEVEL**

## **BACKGROUND OF THE INVENTION**

### **Field of the Invention**

The present invention relates to a method for controlling an  
5 optical power level, which sets up an offset value for a reference  
optical power, and controls laser power levels required for  
individual operation modes of an optical disc apparatus based on  
the offset value.

### **Description of the Related Art**

10 Typically, an optical disc apparatus records data on an optical  
disc such as a CD (Compact Disc) and a DVD (Digital Versatile Disc)  
using a laser diode, or reproduces the data recorded on the optical  
disc.

A power level of the laser diode has been determined when  
15 initially setting up a laser power, and has been constantly maintained  
and generated by an APC (Automatic Power Control) circuit contained  
in an RF (Radio Frequency) IC (Integrated Circuit).

Fig. 1 is a circuit diagram for implementing an APC function of the APC circuit. As shown in Fig. 1, an RF IC 10 receives a feedback signal FPDO from a photo detector (not shown) contained in a pickup unit 20 and an output signal of a DAC (Digital Analog Converter) 102, and outputs its resultant signal while maintaining a constant voltage level at an operational amplifier (OP-AMP) 103.

The DAC 102 receives digital data (hereinafter referred to as a DAC value) from a microcomputer 30, and converts the digital data into analog data or analog signal. In this case, the microcomputer 30 differently sets up the DAC values for individual operation modes, i.e., a data recording mode, a data playback mode, and a disc discrimination mode.

In case of setting up laser power levels according to individual operation modes, the microcomputer 30 sets up the laser power levels on the basis of a DAC offset value. The DAC offset value is an offset value transmitted from the microcomputer 30 to the DAC 102 at a time when an output voltage is about to come into existence from the RF IC 10. If a level shift circuit 101 is further used for the APC circuit shown in Fig. 1, an output voltage shifted by a prescribed voltage is generated as a resultant signal from the RF IC 10.

Fig. 2 is a graph illustrating a conventional method for calculating the DAC offset value. Referring to Fig. 2, the DAC offset value is the X-coordinate of a point at which a linear function graph meets the X-axis. The linear function graph is formed by predetermined DAC values DAC1, and DAC2 transmitted from the microcomputer 30 to the DAC 102, laser power levels P1, and P2 generated from the pickup unit 20 upon receiving the predetermined DAC values DAC1, and DAC2,

or feedback voltage levels FPD01 and FPD02.

The DAC offset value can be calculated by a linear equation shown in the following Equation 1:

5 [Equation 1]

$$rDAC_{offset} = \frac{DAC1 \times P1 - DAC2 \times P2}{P2 - P1},$$

where "rDAC<sub>offset</sub>" is a DAC offset value.

In the meantime, Fig. 3 is a graph illustrating another  
10 conventional method for calculating a DAC offset value. Feedback voltage levels F1 and F2 of a laser diode are proportional to output power values P1 and P2 of a laser, such that a DAC offset value can be calculated by the following Equation 2:

15 [Equation 2]

$$rDAC_{offset} = \frac{DAC1 \times F1 - DAC2 \times F2}{F2 - F1},$$

where "rDAC<sub>offset</sub>" is a DAC offset value.

However, in case of calculating the DAC offset value as above,  
20 laser power levels P1, and P2 and feedback voltage levels FPD01 and FPD02 vary with characteristics and status of the pickup unit 20 shown in Fig. 1, resulting in unavoidably increasing deviations of resultant DAC offset values. Furthermore, because DAC values for individual operation modes, for example, a data recording mode, a

data playback mode, and a disc discrimination mode are determined based on the DAC offset value, DAC values for individual operation modes vary with the DAC offset value, and laser power levels for individual operation modes cannot be appropriately determined.

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#### **SUMMARY OF THE INVENTION**

Therefore, the present invention has been made in view of the above problems, and it is an object of the present invention to provide a method for controlling an optical power level, which more accurately calculates an offset value for a reference optical power needed to set up laser powers for performing individual operation modes, and optimally controls laser power levels for individual operation modes on the basis of the offset value.

In accordance with the present invention, the above and other objects can be accomplished by a method for controlling an optical power level, comprising the steps of: a) regularly increasing a control value of a driving signal generator for driving a pickup unit adapted to output an optical power; b) checking a driving signal of the driving signal generator according to the increasing control value; and c) setting a control value at which the driving signal begins to be generated as an offset value for setting up an optical power.

#### **BRIEF DESCRIPTION OF THE DRAWINGS**

The above and other objects, features and other advantages of the present invention will be more clearly understood from the following detailed description taken in conjunction with the accompanying drawings, in which:

Fig. 1 is a circuit diagram for implementing an APC function of an RF IC contained in an optical disc apparatus;

Fig. 2 is a graph illustrating a conventional method for calculating an offset value for a reference optical power;

5 Fig. 3 is a graph illustrating another conventional method for calculating an offset value for a reference optical power;

Fig. 4 is a graph illustrating a method for setting up an offset value for a reference optical power in accordance with the present invention;

10 Fig. 5 is a flow chart illustrating a method for setting up an offset value for a reference optical power and a method for controlling a desired optical power level on the basis of the offset value in accordance with a preferred embodiment of the present invention;

15 Fig. 6 is a graph illustrating a locus of an output voltage in a threshold area in the output voltage graph shown in Fig. 4; and

Fig. 7 is a flow chart illustrating a method for setting up an offset value for a reference optical power and a method for  
20 controlling a desired optical power level on the basis of the offset value in accordance with another preferred embodiment of the present invention.

#### **DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS**

Now, preferred embodiments of the present invention will be  
25 described in detail with reference to the annexed drawings. In the drawings, the same or similar elements are denoted by the same reference

numerals even though they are depicted in different drawings. In the following description, a detailed description of known functions and configurations incorporated herein will be omitted when it may make the subject matter of the present invention rather unclear.

5        Fig. 4 is a graph illustrating a method for setting up an offset value for a reference optical power when determining a laser power level for each operation mode according to a method for controlling an optical power level of the present invention. Fig. 5 is a flow chart illustrating a method for setting up an offset value for a  
10    reference optical power and a method for controlling a desired optical power level on the basis of the offset value in accordance with a preferred embodiment of the present invention.

      A level shift circuit 101 shown in Fig. 1 shifts an output voltage level to a predetermined voltage level  $V_t$  to stabilize the output  
15    voltage level. Referring to Fig. 4, an output voltage instantaneously increases at an offset point. For example, in case that a shift level determined by the level shift circuit 101 is 0.5V, a DAC offset value at a time of generating an output voltage level of 0.5V can be 64. However, because an RF IC includes an internal offset, and a laser  
20    diode has peculiar characteristics different each other, an output voltage level and a DAC offset value are changed by the internal offset and the characteristics.

      Therefore, the microcomputer 30 changes a DAC value applied to a DAC 102 in the range from a low value to a high value at steps  
25    S10 and S13, and measures an output voltage level of the RF IC 10 at a measuring point at step S11. In more detail, the microcomputer 30 gradually increases the DAC value in the range from "5" to "125"

on the basis of the ideal DAC offset value of 64, and at the same time measures an output voltage level of the RF IC 10.

In this case, if the DAC value reaches a certain value, an output voltage of the RF IC 10 is generated. If the output voltage of the RF IC 10 is generated at step S12, the microcomputer 30 adapts a DAC value applied to the DAC 102 as a DAC offset value at step S14, and stores this DAC offset value in a memory such as an EEPROM (Electrically Erasable Programmable ROM). This DAC offset value stored in the memory is adapted as an offset value for a reference optical power in case of adjusting a laser power level for recording/reproducing data.

In case of recording/reproducing the data on/from an optical disc, or discriminating a type of the optical disc, a laser power should have different output power levels. For example, in case of discriminating the type of an optical disc, a laser power of 1mW is needed. A method for setting up this laser power of 1mW to discriminate the type of such an optical disc will hereinafter be described in detail.

The microcomputer 30 applies a predetermined DAC value to the DAC 102, and measures a laser power generated by an output voltage of the RF IC 10. Based on the offset value for a reference optical power stored in the EEPROM, the predetermined DAC value, and the laser power generated by the predetermined DAC value, the microcomputer 30 calculates a DAC value for outputting a laser power of 1mW at step S15. The microcomputer 30 applies the calculated DAC value to the DAC 102 to generate a laser power of 1mW.

In this case, the DAC value for generating the laser power of 1mW can be calculated by the following Equation 3:

[Equation 3]

$$DAC_{1mW} = DAC_{offset} + \frac{1mW}{P_1} \times (DAC_1 - DAC_{offset}) ,$$

where "DAC<sub>1mW</sub>" is a DAC value at which a laser power of 1mW is  
5 generated;

"DAC<sub>offset</sub>" is a DAC value at an offset point;

"DAC<sub>1</sub>" is a predetermined DAC value applied to the DAC 102 to  
obtain the variable "DAC<sub>1mW</sub>"; and

"P<sub>1</sub>" is a laser power value detected when applying a value of  
10 the "DAC<sub>1</sub>" to the DAC 102.

As stated above, a DAC value for generating a laser power of  
1mW can be set up on the basis of an offset value for a reference  
optical power.

15 However, because an output voltage is very unstable in a threshold  
area where an output level of the RF IC 10 instantaneously increases,  
it is difficult for the microcomputer 30 to obtain a correct DAC offset  
value. A method for more accurately calculating an offset value for  
a reference optical power will hereinafter be described with reference  
20 to preferred embodiments of the present invention.

Fig. 6 is a graph illustrating a locus of an output voltage  
in a threshold area in the output voltage graph shown in Fig. 4.  
Fig. 7 is a flow chart illustrating a method for setting up an offset  
value for a reference optical power and a method for controlling  
25 a desired optical power level on the basis of the offset value  
according to the present invention.



After a DAC value (i.e., a DACTmp value) is measured at a time when an output voltage level escapes from an unstable area, for example, at a time when an output power level becomes 0.2V, a predetermined DAC value is subtracted from this measured DACTmp value in such a way that the offset value for a reference optical power is calculated. It is desirable that the DAC value subtracted from the DACTmp value is "1" or "2".

The microcomputer 30 gradually increases a DAC value in the range from "5" to "125", and applies this increasing DAC value to the DAC 102 at steps S20 and S23 in such a way that an output voltage level is measured at step S21. Then, it is determined whether an output voltage level becomes 0.2V at step S22.

An output voltage level of the RF IC 10 initially maintains a level "0" according to the DAC value applied to the DAC 102, and instantaneously increases in a threshold area shown in Fig. 6. In this case, a predetermined point at which an output voltage of the RF IC 10 becomes 0.2V is found, and a DAC value "DACTmp" of the predetermined point is measured at step S24. After measuring the DACTmp value, a predetermined value such as a number "2" is subtracted from the measured DACTmp value, and the subtracted result value is adapted as an offset value for a reference optical power at step S24.

Then, a predetermined DAC value DAC1 is applied to the DAC 102, and an optical power level P1 generated from a laser diode of the pickup unit 20 is measured at step S25. If the offset value for a reference optical power, the predetermined DAC1, and the measured P1 are applied to the aforementioned equation, a DAC value for

generating a desired laser power is calculated at step S26.

According to individual measurement results of a plurality of laser diodes, a deviation in ratio of an increase of either output power levels or driving signal levels of a laser diode to an increase of DAC values applied to the DAC 102 is a very low value capable  
5 of being sufficiently covered by an optical disc apparatus. In other words, an output power level of a laser diode increases with an almost constant ratio.

So a predetermined value, for example, a DAC value for  
10 generating an optical power of 1mW, can be previously measured in the form of a difference between the DAC value and an offset value, and the measured difference can be previously stored in a memory. And an offset value for a reference optical power is measured or calculated when setting up a laser power. For example, provided that  
15 a DAC value (e.g., offset value + 34) is needed to generate an optical power level of 1mW, a value of 34 is previously stored in the memory, and an offset value is measured or calculated in case of setting up a laser power. Then a DAC input value for generating an optical power of 1mW can be readily calculated by adding the stored value  
20 of 34 and the measured offset value. In this case, the DAC input value for generating a specific power level can also be calculated by a prescribed operation method.

As apparent from the above description, a method for controlling  
25 an optical power level according to the present invention more accurately calculates an offset value for a reference optical power needed to set up laser powers for performing individual operation

modes, and controls optimum laser power levels for individual operation modes on the basis of the offset value.

Although the preferred embodiments of the present invention have been disclosed for illustrative purposes, those skilled in the art will appreciate that various modifications, additions and  
5 substitutions are possible, without departing from the scope and spirit of the invention as disclosed in the accompanying claims.